



## Registration is now extended to EVERYONE who is interested in circuit design.

(The Zoom Webinar Coordinates will be emailed to registrants on Feb. 17, 2022)

09:00 — 09:10	Opening Remarks	Kevin Zhang	ISSCC Conference Chair
	Opening Kendiks	John Long	SSCS President
09:10 - 10:15	Why Circuit Design: Industry Perspective		
09:10 — 09:15	Session Introduction by Daniel Friedman, IBM, ISSCC Short Course Chair		
09:15 — 09:30	Circuits and Silicon at Apple	Jared Zerbe	Apple
09:30 — 09:45	Augmented Reality Applications: Circuit Design Challenges	Edith Beigne	Meta
09:45 — 10:00	Circuit Design to Harness the Power of Scaling and Integration	Stefano Pellerano	Intel
10:00 — 10:15	Our Designs Change the World! – Sustainable Electronics with ST Technology	Andreia Cathelin	ST
10:15 — 10:30	Break		
10:30 - 12:00	Fundamentals of Circuit Design		
10:30 - 11:00	What, Why, and How of Electronic Circuits	Ali Sheikholeslami	Toronto
11:00 — 11:30	Power: The Final Frontier	Mark Horowitz	Stanford
11:30 — 12:00	Precision Analog Circuit Design: Coping with Variability	Kofi Makinwa	Delft
12:00 — 12:15	Break		
12:15 - 13:15	The Joy of Circuit Design		
12:15 — 13:15	Naveen Verma, Princeton ISSCC Tutorials Chair (Moderator)	Asad Abidi	UCLA
		Anantha Chandrakasan	MIT
		Tom Lee	Stanford
		Jan Rabaey	Berkeley
		Willy Sansen	KU Leuven
		Kenneth (KC) Smith	Toronto
13:15 — 13:30	Break		
13:30 - 15:00	Fundamentals of Circuit Design		
13:30 — 14:00	Frequency Behavior of Circuits	Nan Sun	Tsinghua
14:00 — 14:30	Analog Signal Processing	Bram Nauta	Twente
14:30 — 15:00	Positive Attributes of Negative Feedback	Behzad Razavi	UCLA
15:00 — 15:10	Break		
15:10 - 15:40	Fireside Chat with Circuit Designers		
15:10 — 15:40	Frank O'Mahony, Intel ISSCC Forums Chair	Alicia Klinefelter	Nvidia
		Phillip Nadeau	ADI
	(Moderator)	Preet Garcha	TI
		Elsa Lacombe	ST
15:40 — 15:50	Break		
15:50 - 16:50	Fundamentals of Circuit Design		
15:50 — 16:20	Circuits for Machine Learning and Al	Marian Verhelst	KU Leuven
16:20 — 16:50	Circuits for Human Health	Jerald Yoo	Singapore
16:50 - 17:00	Concluding Remarks and Attendees Feedback		