

## **2015 Lewis Winner Award for Outstanding Paper**

*“A Highly-Digital Frequency Synthesizer Using Ring-Oscillator Frequency-to Digital Conversion and Noise Cancellation”*

Colin Weltin-Wu<sup>1,2</sup>, Guobi Zhao<sup>2</sup>, Ian Galton<sup>2</sup>

<sup>1</sup>Analog Devices, San Jose, CA

<sup>2</sup>University of California, San Diego, CA

## **2015 Distinguished-Technical-Paper Award**

*“A 3nW Signal-Acquisition IC Integrating an Amplifier with 2.1 NEF and a 1.5ff/conv-step ADC”*

Pieter Harpe, Hao Gao, Rainier van Dommele, Eugenio Cantatore, Arthur van Roermund

Eindhoven University of Technology, Eindhoven, The Netherlands

## **2015 Jan Van Vesseem Award for Outstanding European Paper**

*“A Transmitter with 10b 128MS/s Incremental-Charge-Based DAC Achieving -155dBc/Hz Out-of-Band Noise”*

Pedro Emiliano Paro Filho<sup>1,2</sup>, Mark Ingels<sup>1</sup>, Piet Wambacq<sup>1,2</sup>, Jan Craninckx<sup>1</sup>

<sup>1</sup>imec, Leuven, Belgium

<sup>2</sup>Vrije Universiteit Brussel, Brussels, Belgium

## **2015 Takuo Sugano Award for Outstanding Far-East Paper**

*“An LTE SAW-Less Transmitter Using 33% Duty-Cycle LO Signals for Harmonic Suppression”*

Yen-Horng Chen<sup>1</sup>, Neric Fong<sup>2</sup>, Bing Xu<sup>3</sup>, Caiyi Wang<sup>3</sup>

<sup>1</sup>MediaTek, Hsinchu, Taiwan

<sup>2</sup>MediaTek, San Jose, CA

<sup>3</sup>MediaTek, Austin, TX

## **2015 ISSCC Award for Outstanding Forum Presenter**

*“Power-Efficient Design Approaches for >60Gb/s Transceivers”*

Chih-Kong Ken Yang, University of California, Los Angeles, CA

## **2015 Evening Session Award**

*“Moore’s Law Challenges Below 10nm: Technology, Design and Economic Implications”*

Organizer/Moderator: Dimitri Antoniadis, MIT, Cambridge, MA

Co-Organizers:

Bing Sheu, TSMC, Hsinchu, Taiwan

Kathy Wilcox, AMD, Boxborough, MA

Ali Keshavarzi, Cypress, San Jose, CA

Panelists:

Mark Bohr, Intel, Hillsboro, OR

Jack Sun, TSMC, Hsinchu, Taiwan

Liam Madden, Xilinx, San Jose, CA

Mark Hill, University of Wisconsin, Madison, WI

Geoffrey Yeap, Qualcomm, San Diego, CA

Jo De Boeck, imec, Heverlee, Belgium

## **2015 Evening Session Award (Special Recognition)**

*“Lost Art? Analog Tricks and Techniques from the Masters”*

Organizer/Moderator: Richard Schreier, Analog Devices, Toronto, Canada

Co-Organizers:

Tsung-Hsien Lin, National Taiwan University, Taipei, Taiwan

Carlo Samori, Politecnico di Milano, Milano, Italy

Richard Schreier, Analog Devices, Toronto, Canada

Panelists:

Asad Abidi, University of California Los Angeles, Los Angeles, CA

Barrie Gilbert, Analog Devices, Portland, OR

Jan Craninckx, IMEC, Leuven, Belgium

Shoji Kawahito, Shizuoka University, Shizuoka, Japan

Rinaldo Castello, University of Pavia, Pavia, Italy  
Huei Wang, National Taiwan University, Taipei, Taiwan

## **2015 Demonstration Session Certificate of Recognition**

*“A 2.71nJ/Pixel 3D-Stacked Gaze-Activated Object-Recognition System for Low-Power Mobile HMD Applications”*

Injoon Hong, Kyeongryeol Bong, Dongjoo Shin, Seongwook Park, Kyuho Lee, Youchang Kim, Hoi-Jun Yoo

KAIST, Daejeon, Korea

## **2015 Demonstration Session Certificate of Recognition**

*“A 240Hz-Reporting-Rate Mutual-Capacitance Touch-Sensing Analog Front-End Enabling Multiple Active/Passive Styluses with 41dB/32dB SNR for 0.5mm Diameter”*

Mutsumi Hamaguchi, Michiaki Takeda, Masayuki Miyamoto

SHARP, Tenri, Japan

## **2016 Silkroad Award**

*“A 185fsrms-Integrated-Jitter and -245dB FOM PVT-Robust Ring-VCO Based Injection-Locked Clock Multiplier with a Continuous Frequency-Tracking Loop Using a Replica-Delay Cell and a Dual-Edge Phase Detector”*

Seojin Choi, Seyeon Yoo, Jaehyouk Choi